

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
3 February 2005 (03.02.2005)

PCT

(10) International Publication Number
WO 2005/010611 A2

(51) International Patent Classification⁷: **G03F**
(21) International Application Number:
PCT/US2004/017452
(22) International Filing Date: 2 June 2004 (02.06.2004)
(25) Filing Language: English
(26) Publication Language: English
(30) Priority Data:
60/485,868 8 July 2003 (08.07.2003) US

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

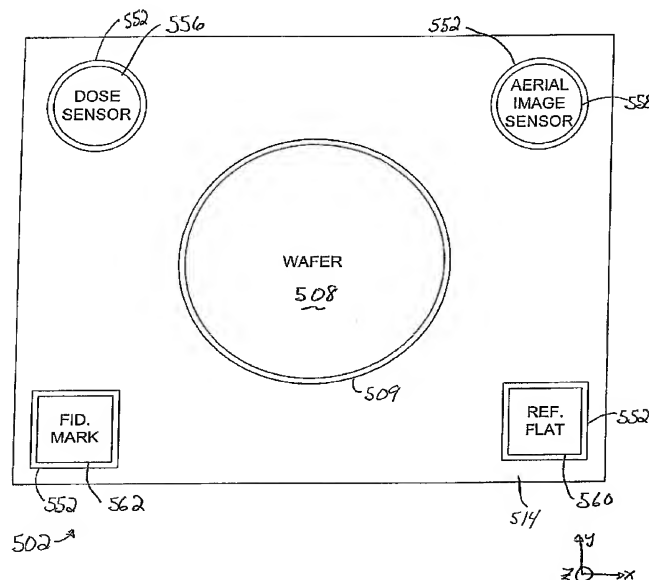
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— without international search report and to be republished upon receipt of that report

[Continued on next page]

(54) Title: **WAFER TABLE FOR IMMERSION LITHOGRAPHY**



(57) Abstract: Methods and apparatus for allowing a liquid to be substantially contained between a lens and a wafer table assembly of an immersion lithography system are disclosed. According to one aspect of the present invention, an exposure apparatus includes a lens and a wafer table assembly. The wafer table assembly has a top surface, and is arranged to support a wafer to be moved with respect to the lens as well as at least one component. The top surface of the wafer and the top surface of the component are each at substantially a same height as the top surface of the wafer table assembly. An overall top surface of the wafer table assembly which includes the top surface of the wafer, the top surface of the wafer table assembly, and the top surface of the at least one component is substantially planar.

WO 2005/010611 A2

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
3 February 2005 (03.02.2005)

PCT

(10) International Publication Number
WO 2005/010611 A3

(51) International Patent Classification⁷: **G03B 27/58**

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(21) International Application Number:
PCT/US2004/017452

(81) Designated States (*unless otherwise indicated, for every kind of national protection available*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(22) International Filing Date: 2 June 2004 (02.06.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/485,868 8 July 2003 (08.07.2003) US

(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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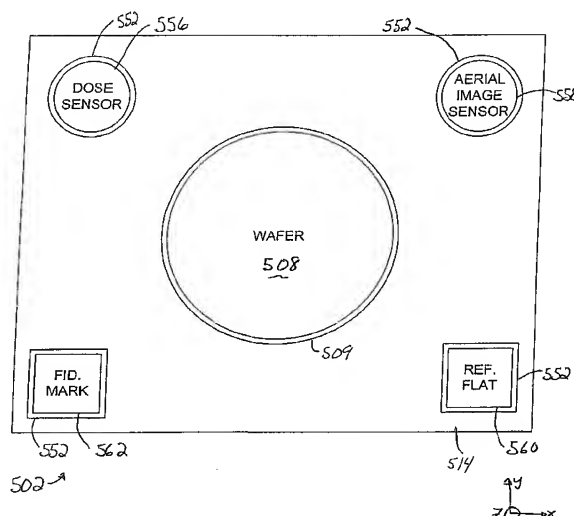
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Published:
— with international search report

[Continued on next page]

(54) Title: **WAFER TABLE FOR IMMERSION LITHOGRAPHY**



(57) Abstract: Methods and apparatus for allowing a liquid to be substantially contained between a lens (46) and a wafer table (51) assembly of an immersion lithography system are disclosed. According to one aspect of the present invention, an exposure apparatus includes a lens (46) and a wafer table (51) assembly. The wafer table (51) assembly has a top surface, and is arranged to support a wafer (64) to be moved with respect to the lens (46) as well as at least one component (350). The top surface of the wafer (64) and the top surface of the component (350) are each at substantially a same height as the top of the wafer table (51) assembly. An overall top surface of the wafer table (51) assembly which includes the top surface of the wafer (64), the top surface of the wafer table (51) assembly, and the top surface of the at least one component (350) is substantially planar.

WO 2005/010611 A3



— *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

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(88) Date of publication of the international search report:
15 September 2005

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US04/17452

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : G03B 27/58

US CL : 355/72

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 355/53, 72, 75

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5,825,043 A (SUWA) 20 October 1998 (20.10.1998), see entire document.	1-33

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

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Date of the actual completion of the international search

20 May 2005 (20.05.2005)

Date of mailing of the international search report

Name and mailing address of the ISA/US

Mail Stop PCT, Attn: ISA/US
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

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WAFER TABLE FOR IMMERSION LITHOGRAPHY

CROSS-REFERENCE TO RELATED APPLICATION

This application claims priority to U.S. Provisional Patent Application No. 60/485,868, filed July 8, 2003, which is hereby incorporated by reference in its entirety.

BACKGROUND OF THE INVENTION

5 1. Field of Invention

The present invention relates generally to semiconductor processing equipment. More particularly, the present invention relates to a method and an apparatus for enabling liquid in an immersion lithography system to effectively be contained between a surface of a lens and a plane that is moved relative to the lens.

10 2. Description of the Related Art

For precision instruments such as photolithography machines which are used in semiconductor processing, factors which affect the performance, *e.g.*, accuracy, of the precision instrument generally must be dealt with and, insofar as possible, eliminated. When the performance of a precision instrument such as an immersion lithograph
15 exposure system is adversely affected, products formed using the precision instrument may be improperly formed and, hence, function improperly.

In an immersion lithography system, a liquid is provided between a lens and the surface of a wafer in order to improve the imaging performance of the lens. The use of liquid allows a numerical aperture associated with the lens, *i.e.*, an effective numerical
20 aperture of the lens, to essentially be increased substantially without altering characteristics of the lens, since a liquid such as water generally has a refractive index that is greater than one. In general, a higher numerical aperture enables a sharper image to be formed on the wafer. As will be appreciated by those skilled in the art, a high refractive index liquid allows for a high numerical aperture of the lens since an effective
25 numerical aperture of a lens system of an immersion lithography system is generally defined to be approximately equal to the sine of an angle of diffraction of light which passes through a lens and reflects off a surface multiplied by the refractive index of the liquid. Since the refractive index of the liquid is greater than one, the use of liquid allows the effective numerical aperture of the lens to be increased, thereby enabling the
30 resolution associated with the lens to essentially be improved.

Within most conventional lithography systems, air is present between a lens and a surface which passes under the lens, *e.g.*, the surface of a wafer. In such systems, the numerical aperture associated with the lens is often in the range of approximately 0.8 to 0.9. Increasing the numerical aperture of a lens to achieve an improved resolution is generally impractical, as the diameter of a lens generally must be increased, which adds significant difficulty to a lens manufacturing process. In addition, the numerical aperture of a lens in air is theoretically limited to one, and, in practice, is limited to being somewhat less than one. Hence, immersion lithography systems enable the effective numerical aperture of a lens to be increased substantially beyond what is possible with a lens in air.

Fig. 1 is a diagrammatic cross-sectional representation of a portion of an immersion lithography apparatus. An immersion lithography apparatus 100 includes a lens assembly 104 which is positioned over a wafer table 112 which supports a wafer 108. Wafer table 112 is arranged to be scanned or otherwise moved under lens assembly 104. A liquid 116, which may be water in a typical application which uses approximately 193 nanometers (nm) of radiation, is present in a gap between lens assembly 104 and wafer 108. In order to effectively prevent liquid 116 from leaking out from under lens assembly 104, *i.e.*, to effectively laterally contain liquid 116 between lens assembly 104 and wafer 108, a retaining ring 120 may be positioned such that retaining ring 120 enables liquid 116 to remain between lens assembly 104 and wafer 108, and within an area defined by retaining ring 120.

While retaining ring 120 is generally effective in containing liquid 116 when lens assembly 104 is positioned such that a small gap between retaining ring 120 and a surface of wafer 108 is maintained, for a situation in which at least a part of retaining ring 120 is above wafer 108, liquid 116 may leak out from between lens assembly 104 and wafer 108. By way of example, when an edge of wafer 108 is to be patterned, lens assembly 104 may be substantially centered over the edge such that a portion of retaining ring 120 fails to maintain the small gap under the bottom surface of retaining ring 120, and liquid 116 is allowed to leak out from between lens assembly 104 and wafer 108. As shown in Fig. 2, when lens assembly 104 is positioned such that at least part of a bottom surface of retaining ring 120 is not in contact with wafer 108, liquid 116 may not be contained in an area defined by retaining ring 120 between lens assembly 104 and wafer 108.

In an immersion lithography apparatus, a wafer table may support sensors and other components, *e.g.*, a reference flat that is used to calibrate automatic focusing operations. Such sensors and other components generally may be positioned beneath a

lens at some point. That is, sensors and other components associated with a wafer table may be occasionally positioned beneath a lens during the course of operating the lens and the wafer table. While the use of a retaining ring may prevent liquid from leaking out of a gap between a lens assembly and the top surface of the wafer, liquid may leak out from between the lens assembly and top surfaces of sensors and other components when the lens assembly is positioned over the sensors or other components.

Fig. 3 is a block diagram representation of a wafer table which supports a sensor and a wafer holder that holds a wafer. A wafer table 312 supports a wafer holder 310 which is arranged to hold a wafer (not shown), a sensor 350, and an interferometer mirror 352. Sensor 350 may be used through a lens (not shown) with liquid (not shown) between the lens and sensor 350. However, liquid will often flow out of the gap between a lens (not shown) and sensor 350 particularly when an edge of sensor 350 is positioned substantially beneath a center of the lens. The effectiveness of sensor 350 may be compromised when sensor 350 is designed and calibrated to operate in a liquid, and there is insufficient liquid present between a lens (not shown) and sensor 350. Further, when liquid (not shown) flows out of the gap between a lens (not shown) and sensor 350, the liquid which flowed out of the gap is effectively lost such that when the lens is subsequently positioned over a wafer (not shown) supported by wafer holder 310, the amount of liquid between the lens and the wafer may not be sufficient to enable the effective numerical aperture of the lens to be as high as desired. Hence, when liquid is not successfully contained between a lens (not shown) and sensor 350 while sensor 350 is at least partially positioned under the lens, an overall lithography process which involves the lens and sensor 350 may be compromised.

Therefore, what is needed is a method and an apparatus for allowing liquid to be maintained in a relatively small gap defined between a surface of a lens and a surface of substantially any sensors or components which are supported by a wafer table. That is, what is desired is a system which is suitable for preventing liquid which is positioned between a lens and substantially any surface on a wafer table which is moved under the lens from leaking out from between the lens and the surface.

SUMMARY OF THE INVENTION

The present invention relates to a wafer table arrangement which is suitable for use in an immersion lithography system. According to one aspect of the present invention, an exposure apparatus includes a lens and a wafer table assembly. The wafer table assembly has a top surface, and is arranged to support a wafer to be moved with

respect to the lens as well as at least one component. The top surface of the wafer and the top surface of the component are each at substantially a same height as the top surface of the wafer table assembly. An overall top surface of the wafer table assembly which includes the top surface of the wafer, the top surface of the wafer table assembly, and the top surface of the at least one component is substantially planar.

In one embodiment, the component may be at least one of a reference flat, an aerial image sensor, a dose sensor, and a dose uniformity sensor. In another embodiment, the wafer table assembly is arranged to support a wafer holder that holds the wafer such that the top surface of the wafer is at substantially the same height as the top surface of the wafer table assembly.

A wafer table arrangement which is configured to enable surfaces which are to viewed through a lens to form a relatively planar overall surface of substantially the same height facilitates an immersion lithography process. When substantially all elements carried on a wafer table have top surfaces that are substantially level with the top surface of the wafer table, and any gaps between the sides of the components and the sides of openings in the wafer table are relatively small, the overall top surface of a wafer table arrangement may traverse under a lens while a layer or a film of liquid is effectively maintained between a surface of the lens and the overall top surface. Hence, an immersion lithography process may be performed substantially without the integrity of the layer of liquid between the surface of the lens and the overall top surface of the wafer table arrangement being compromised by the loss of liquid from the layer of liquid between the surface of the lens and the overall top surface of the wafer table arrangement.

According to another aspect of the present invention, an immersion lithography apparatus includes a lens which has a first surface and an associated effective numerical aperture. The apparatus also includes a liquid that is suitable for enhancing the effective numerical aperture of the lens, and a table arrangement. The table arrangement has a substantially flat top surface that opposes the first surface, and the liquid is arranged substantially between the substantially flat top surface and the first surface. The substantially flat top surface includes a top surface of an object to be scanned and a top surface of at least one sensor.

These and other advantages of the present invention will become apparent upon reading the following detailed descriptions and studying the various figures of the drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention may best be understood by reference to the following description taken in conjunction with the accompanying drawings in which:

Fig. 1 is a diagrammatic cross-sectional representation of a portion of an immersion lithography apparatus in a first orientation.

Fig. 2 is a diagrammatic representation cross-sectional representation of a portion of an immersion lithography apparatus, *i.e.*, apparatus 100 of Fig. 1, in a second orientation.

Fig. 3 is a block diagram representation of a wafer table which supports a sensor and a wafer holder that holds a wafer.

Fig. 4 is a block diagram representation of a top view of a wafer table assembly in accordance with an embodiment of the present invention.

Fig. 5 is a block diagram representation of a top view of components which are supported by a wafer table assembly with a substantially uniform, planar overall top surface in accordance with an embodiment of the present invention.

Fig. 6a is a diagrammatic cross-sectional representation of a wafer table assembly which has a substantially uniform, planar overall top surface in accordance with an embodiment of the present invention.

Fig. 6b is a diagrammatic representation of a wafer table assembly, *e.g.*, wafer table assembly 600 of Fig. 6a, with a lens assembly positioned over a wafer holder in accordance with an embodiment of the present invention.

Fig. 6c is a diagrammatic representation of a wafer table assembly, *e.g.*, wafer table assembly 600 of Fig. 6a, with a lens assembly positioned over a component in accordance with an embodiment of the present invention.

Fig. 7 is a diagrammatic representation of a photolithography apparatus in accordance with an embodiment of the present invention.

Fig. 8 is a process flow diagram which illustrates the steps associated with fabricating a semiconductor device in accordance with an embodiment of the present invention.

Fig. 9 is a process flow diagram which illustrates the steps associated with processing a wafer, *i.e.*, step 1304 of Fig. 4, in accordance with an embodiment of the present invention.

Fig. 10a is a diagrammatic representation of a wafer table surface plate and a wafer table in accordance with an embodiment of the present invention.

Fig. 10b is a diagrammatic cross-sectional representation of wafer table assembly which includes a wafer table and a wafer table surface plate in accordance with an embodiment of the present invention.

Fig. 11a is a diagrammatic cross-sectional representation of a wafer table and a
5 wafer table surface plate with windows in accordance with an embodiment of the present invention.

Fig. 11b is a diagrammatic cross-sectional representation of wafer table assembly which includes a wafer table and a wafer table surface plate with windows, i.e., wafer
10 table 904 and wafer table surface plate 908 of Fig. 11a, in accordance with an embodiment of the present invention.

DETAILED DESCRIPTION OF THE EMBODIMENTS

In immersion lithography systems, a wafer surface must generally be viewed through a lens with a layer of liquid such as a relatively thin film of liquid between the lens and the wafer surface. Some components such as sensors and/or reference members
15 may often be viewed through the lens with a layer of liquid between the lens and the surfaces of the components. Maintaining the layer of liquid in a gap between the lens and the surface of the wafer, even when the lens is arranged to view edge portions of the wafer, allows the immersion lithography system to operate substantially as desired. Similarly, maintaining the layer of liquid in a gap between the lens and the surface of
20 components such as sensors and/or reference members also facilitates the efficient operation of the immersion lithography system.

By utilizing a wafer table arrangement which is configured to enable surfaces which are to viewed through a lens and a top surface of the wafer table arrangement to form a relatively planar, substantially uniform overall surface. When substantially all
25 components associated with the wafer table arrangement have a surface which is substantially level with the top surface of a wafer and the top surface of a wafer table, and any gaps between the sides of the components and the sides of openings in the wafer table are relatively small, the overall top surface of the wafer table arrangement may traverse under a lens while a layer or a film of liquid is effectively maintained between a surface
30 of the lens and the overall top surface. As a result, an immersion lithography process may be performed substantially without having the integrity of the layer of liquid, i.e., the layer of liquid between the surface of the lens and the overall top surface of the wafer table arrangement, compromised.

A wafer table which has a substantially raised, flat top surface of a uniform height allows a wafer and other components, *e.g.*, sensors, to be installed such that a flat surface of the wafer and flat surfaces of the components are at substantially the same level or height as the raised, flat top surface of the wafer table. In one embodiment, an overall
5 wafer table assembly includes openings within which a wafer, or a wafer holder on which the wafer is supported, and sensors may be positioned. Fig. 4 is a block diagram representation of a top view of a wafer table assembly in accordance with an embodiment of the present invention. A wafer table assembly 402 includes openings 409, 452 which are sized to effectively house a wafer 408 and components 450, respectively, such that top
10 surfaces of wafer 408 and components 450 are at substantially the same level as a top surface 414 of wafer table assembly 402. Typically, openings 409, 452 are sized to accommodate wafer 408 and components 450, respectively, such that a spacing between the outer edges of wafer 408 or components 450 and their respective openings 409, 452 is relatively small, *e.g.*, between approximately ten and approximately 500 micrometers.

15 In general, the spacing between wafer 408, or in some cases, a wafer holder (not shown) and opening 409, as well as the spacing between components 450 and their corresponding openings 452 does not significantly affect the overall planar quality of an overall top surface of wafer table assembly 402. That is, an overall top surface of wafer table assembly 402 which includes top surface 414, the top surface of wafer 408, and the
20 top surfaces of components 450 is substantially planar, with the planarity of the overall top surface being substantially unaffected by the presence of the small gaps between the sides of wafer 408 and opening 409, and the sides of components 450 and openings 452.

Components 450 may include, but are not limited to, various sensors and reference marks. With reference to Fig. 5, components which are supported by a wafer
25 table assembly with a substantially uniform, planar overall top surface will be described in accordance with an embodiment of the present invention. A wafer table assembly 502 includes a raised, substantially uniform top surface 514 in which openings 509, 552 are defined. Opening 509 is arranged to hold a wafer 508 which may be supported by a wafer holder (not shown). Openings 552 are arranged to support any number of
30 components. In the described embodiment, openings 552 are arranged to support a dose sensor or a dose uniformity sensor 556, an aerial image sensor 558, a reference flat 560, and a fiducial mark 562, which each have top surfaces that are arranged to be of substantially the same height as top surface 514 such that an overall, substantially uniform, planar top surface is formed. Examples of a suitable dose sensor or dose
35 uniformity sensor 556 are described in U.S. Patent No. 4,465,368, U.S. Patent No.

6,078,380, and U.S. Patent Publication No. 2002/0061469A1, which are each incorporated herein by reference in their entireties. An example of an aerial image sensor 558 is described in U.S. Patent Publication No. 2002/0041377A1, which is incorporated herein by reference in its entirety. An example of a reference flat 560 is described in U.S. Patent No. 5,985,495, which is incorporated herein by reference in its entirety, while an example of a fiducial mark 562 is described in U.S. Patent No. 5,243,195, which is incorporated herein by reference in its entirety.

It should be appreciated that openings 552 are sized to accommodate components such that gaps between the sides of components, as for example dose sensor or dose uniformity sensor 556, aerial image sensor 558, reference flat 560, and fiducial mark 562, and edges of openings 552 are not large enough to significantly affect the uniformity and planarity of the overall top surface. In other words, components are relatively tightly fit within openings 552.

Dose sensor or a dose uniformity sensor 556, one or both of which may be included in openings 552, is arranged to be used to determine a strength of a light source associated with a lens assembly (not shown) by studying light energy at the level of the top surface of wafer 508. In one embodiment, only a dose uniformity sensor is typically included. A dose sensor generally measures absolute illumination intensity, while a dose uniformity sensor typically measures variations over an area. As such, dose sensor or does uniformity sensor 556 is positioned in the same plane as the top surface of wafer 508. Aerial image sensor 558 is arranged to effectively measure an aerial image that is to be projected onto the surface of wafer 508 and, hence, exposed on photoresist. In order for aerial image sensor to accurately measure an aerial image, aerial image sensor 558 is essentially positioned at the same level or plane as the top surface of wafer 508.

Reference flat 560 is generally used to calibrate the automatic focus functionality of a lens assembly (not shown), while fiducial mark 562 is a pattern that is used to enable wafer 508 to be aligned with respect to the lens assembly and reticle, as will be understood by those skilled in the art. Both reference flat 560 and fiducial mark 562 are positioned in the same plane as wafer 508.

Fig. 6a is a diagrammatic cross-sectional representation of a wafer table assembly which has a substantially uniform, planar overall top surface in accordance with an embodiment of the present invention. An overall wafer table assembly 600 includes a wafer table 602 which is arranged to support a wafer holder 608 that holds a wafer (not shown) such that a top surface of the wafer is substantially flush with an overall top surface 614 of wafer table assembly 600. Wafer table 602 also supports components 650,

which may include sensors and reference marks, such that top surfaces of components 650 are also substantially flush with overall top surface 614, as discussed above. In other words, top surfaces of components 650, wafer holder 608 when supporting a wafer (not shown), and wafer table 602 effectively form a substantially flat overall top surface 614 of relatively uniform height. Wafer holder 608 and components 650 are arranged to be relatively tightly fit into openings defined within wafer table 602 such that a gap between the side of wafer holder 608 and the sides of an associated opening within wafer table 602, as well as gaps between components 650 and the sides of associated openings within wafer table 602, are each relatively small, and do not have a significant effect on the uniformity of overall top surface 614.

Wafer table 602 may support additional components or elements in addition to wafer holder 608, a wafer (not shown), and components 650. By way of example, wafer table 602 may support an interferometer mirror 670. It should be appreciated that a top surface of interferometer mirror 670 may also be substantially level with overall top surface 614. Hence, in one embodiment, overall top surface 614 may include interferometer mirror 670.

Overall top surface 614 enables liquid to be maintained in a gap between a lens assembly and overall top surface 614 when a component 650 or wafer holder 608 traverses beneath the lens. Fig. 6b is a diagrammatic representation of a wafer table assembly, *e.g.*, wafer table assembly 600 of Fig. 6a, which is arranged to scan beneath a lens assembly in accordance with an embodiment of the present invention. A lens assembly 684, which is arranged to be positioned over overall top surface 614 is effectively separated from overall top surface 614 along a z-axis 690a by a layer of liquid 682. The size of the immersion area covered by liquid 682 is relatively small, *e.g.*, the size of the immersion area may be smaller than that of the wafer (not shown). Local fill methods which are used to provide the liquid of the immersion area are described in co-pending PCT International Patent Application No. PCT/US04/10055 (filed March 29, 2004), co-pending PCT International Patent Application No. PCT/US04/09994 (filed April 1, 2004), and co-pending PCT International Patent Application No. PCT/US04/10071 (filed April 1, 2004), which are each incorporated herein by reference in their entireties. Layer of liquid 682 is effectively held between overall top surface 614 and lens assembly 684 with respect to an x-axis 690b and a y-axis 690c by a retaining ring 680, although substantially any suitable arrangement may be used to effectively hold layer of liquid 682 in place relative to x-axis 690b and y-axis 690c. Retaining ring 680 is arranged as a ring-like structure with respect to x-axis 690b and y-axis 690c which

contains liquid 682 in an area defined by the edges of retaining ring 680. That is, retaining ring 680 forms a ring-like shape about z-axis 690a.

In another embodiment, retaining ring 680 may not be necessary. If the gap between lens assembly 684 and overall top surface 614 (or wafer surface) is relatively small, e.g., between approximately 0.5 mm and approximately 5 mm, layer of liquid 682 may be effectively held between the gap with surface tension of liquid 682.

In general, liquid 682 may be substantially any suitable liquid which fills a gap or a space between a surface of lens assembly 684 and overall top surface 614 within an area defined by retaining ring 680 that allows an effective numerical aperture of a lens included in lens assembly 684 to be increased for the same wavelength of light and the same physical size of the lens. Liquids including various oils, e.g., Fomblin™ oil, may be suitable for use as liquid 682. In one embodiment, as for example within an overall system which uses approximately 193 nanometers (nm) of radiation, liquid 682 is water. However, for shorter wavelengths, liquid 682 may be an oil.

Since overall top surface 614 is substantially flat and uniform, when lens assembly 684 is positioned over wafer holder 608, liquid 682 does not leak out from between overall top surface 614 and lens assembly 684, since retaining ring 680 remains in contact or in close proximity with overall top surface 614, even when lens assembly 684 is positioned over an edge of wafer holder 608. The uniformity and planarity of overall top surface 614 also allows liquid 682 to remain between lens assembly 684 and overall top surface 614 when lens assembly 684 is oriented over a component 650, as shown in Fig. 6c.

While a wafer table arrangement may include a wafer table in which openings have been defined to house a wafer or a wafer holder and any number of components, a wafer table arrangement may instead include a wafer table which has no openings to house a wafer or a wafer holder and any number of components and structures which may cooperate with the wafer table to effectively form openings in which a wafer or a wafer holder and any number of components may be placed. In other words, a substantially planar wafer table arrangement may either include openings formed within a wafer table as discussed above, or openings defined by a structure or structures positioned atop a wafer table.

When a wafer table arrangement includes a structure that defines openings which may effectively house a wafer and components and provides a substantially planar top surface for the wafer table arrangement, the structure may generally be a plate-like structure within which openings are formed. With reference to Fig. 10a, a wafer table

arrangement which includes a wafer table and a wafer table surface plate will be described in accordance with an embodiment of the present invention. A wafer table arrangement 700 includes a wafer table 704 and a wafer table surface plate 708, Wafer table 704 supports a wafer 712 and one or more components 716 which may include various sensors, a fiducial mark, or a reference flat. Wafer table surface plate 708, which may be formed from any suitable material, *e.g.*, Teflon, includes an opening 720 within which wafer 712, may be positioned when wafer table surface plate 708 is positioned atop wafer table 704. Openings 724, which are also defined in wafer table surface plate 708, are arranged to fit around components 716 when wafer table surface plate 708 is positioned atop wafer table 704.

Wafer table surface plate 708 includes a top surface which, when wafer table surface plate 708 is positioned atop wafer table 704 such that wafer 712 is positioned within opening 720 and components 716 are positioned within openings 724, cooperates with a top surface of wafer 712 and top surfaces of components 716 to create a substantially uniform, planar overall top surface. As shown in Fig. 10b, when a wafer table surface plate 808 is positioned over a wafer table 804, a top surface of components 816, *e.g.*, sensors, and a top surface of a wafer 812 are at substantially the same height as a top surface of wafer table surface plate 708. In the embodiment as shown, an interferometer mirror 814 also has a top surface that is at substantially the same height as the top surface of wafer table surface plate 808. Hence, an overall wafer table arrangement which includes wafer table 804 and wafer table surface plate 808 has an overall top surface that is relatively uniform and planar.

While a wafer table arrangement may include a wafer table in which openings have been defined to house a wafer or a wafer holder and any number of components, a wafer table arrangement may instead include a wafer table which has no openings to house a wafer or a wafer holder and any number of components and structures which may cooperate with the wafer table to effectively form openings in which a wafer or a wafer holder and any number of components may be placed. In other words, a substantially planar wafer table arrangement may either include openings formed within a wafer table as discussed above, or openings defined by a structure or structures positioned atop a wafer table.

A plate-like structure which enables a substantially planar overall surface of a wafer table arrangement to be achieved may vary widely. In one embodiment, sensors or components may be integral to the plate-like structure. By way of example, a reference flat or a fiducial mark may be etched or otherwise formed directly onto the plate-like

structure. In another embodiment, the plate-like structure may include an opening within which a wafer supported by a wafer holder may be held, and openings topped by windows which may protect sensors while allowing sensors to function. With reference to Figs. 11a and 11b, a wafer table arrangement which includes a wafer table and a wafer table surface plate with windows will be described in accordance with an embodiment of the present invention. A wafer table arrangement 900 includes a wafer table 904 and a wafer table surface plate 908 which includes window 916 which are arranged to be positioned over one or more components 916 such as sensors, as shown in Fig. 11b. Wafer table 904 supports a wafer 912, which is arranged to fit into an opening in plate 908.

Plate 908 may be formed from any suitable material, *e.g.*, Teflon, with windows 960 being formed from a clear material. Alternatively, plate 908 may be a clear cover plate with windows 960 being relatively thin portions of plate 908 positioned over openings 924 which are arranged to fit around components 916 when wafer table surface plate 908 is positioned atop wafer table 904. A top surface of plate 908 cooperates with a top surface of wafer 912 to form a substantially planar top surface for arrangement 900.

With reference to Fig. 7, a photolithography apparatus which may be part of an immersion lithography exposure system that includes a wafer table assembly with a flat surface of substantially uniform height will be described in accordance with an embodiment of the present invention. A photolithography apparatus (exposure apparatus) 40 includes a wafer positioning stage 52 that may be driven by a planar motor (not shown), as well as a wafer table 51 that is magnetically coupled to wafer positioning stage 52 by utilizing an EI-core actuator, *e.g.*, an EI-core actuator with a top coil and a bottom coil which are substantially independently controlled. The planar motor which drives wafer positioning stage 52 generally uses an electromagnetic force generated by magnets and corresponding armature coils arranged in two dimensions. A wafer 64 is held in place on a wafer holder or chuck 74 which is coupled to wafer table 51. Wafer positioning stage 52 is arranged to move in multiple degrees of freedom, *e.g.*, between three to six degrees of freedom, under the control of a control unit 60 and a system controller 62. In one embodiment, wafer positioning stage 52 may include a plurality of actuators which are coupled to a common magnet track. The movement of wafer positioning stage 52 allows wafer 64 to be positioned at a desired position and orientation relative to a projection optical system 46.

Wafer table 51 may be levitated in a z-direction 10b by any number of voice coil motors (not shown), *e.g.*, three voice coil motors. In the described embodiment, at least

three magnetic bearings (not shown) couple and move wafer table 51 along a y-axis 10a. The motor array of wafer positioning stage 52 is typically supported by a base 70. Base 70 is supported to a ground via isolators 54. Reaction forces generated by motion of wafer stage 52 may be mechanically released to a ground surface through a frame 66. One
5 suitable frame 66 is described in JP Hei 8-166475 and U.S. Patent No. 5,528,118, which are each herein incorporated by reference in their entirety.

An illumination system 42 is supported by a frame 72. Frame 72 is supported to the ground via isolators 54. Illumination system 42 includes an illumination source, and is arranged to project a radiant energy, *e.g.*, light, through a mask pattern on a reticle 68
10 that is supported by and scanned using a reticle stage which includes a coarse stage and a fine stage. The radiant energy is focused through projection optical system 46, which is supported on a projection optics frame 50 and may be supported the ground through isolators 54. Suitable isolators 54 include those described in JP Hei 8-330224 and U.S. Patent No. 5,874,820, which are each incorporated herein by reference in their entirety.

15 A first interferometer 56 is supported on projection optics frame 50, and functions to detect the position of wafer table 51. Interferometer 56 outputs information on the position of wafer table 51 to system controller 62. In one embodiment, wafer table 51 has a force damper which reduces vibrations associated with wafer table 51 such that interferometer 56 may accurately detect the position of wafer table 51. A second
20 interferometer 58 is supported on projection optical system 46, and detects the position of reticle stage 44 which supports a reticle 68. Interferometer 58 also outputs position information to system controller 62.

It should be appreciated that there are a number of different types of photolithographic apparatuses or devices. For example, photolithography apparatus 40,
25 or an exposure apparatus, may be used as a scanning type photolithography system which exposes the pattern from reticle 68 onto wafer 64 with reticle 68 and wafer 64 moving substantially synchronously. In a scanning type lithographic device, reticle 68 is moved perpendicularly with respect to an optical axis of a lens assembly (projection optical system 46) or illumination system 42 by reticle stage 44. Wafer 64 is moved
30 perpendicularly to the optical axis of projection optical system 46 by a wafer stage 52. Scanning of reticle 68 and wafer 64 generally occurs while reticle 68 and wafer 64 are moving substantially synchronously.

Alternatively, photolithography apparatus or exposure apparatus 40 may be a step-and-repeat type photolithography system that exposes reticle 68 while reticle 68 and
35 wafer 64 are stationary, *i.e.*, at a substantially constant velocity of approximately zero

meters per second. In one step and repeat process, wafer 64 is in a substantially constant position relative to reticle 68 and projection optical system 46 during the exposure of an individual field. Subsequently, between consecutive exposure steps, wafer 64 is consecutively moved by wafer positioning stage 52 perpendicularly to the optical axis of projection optical system 46 and reticle 68 for exposure. Following this process, the images on reticle 68 may be sequentially exposed onto the fields of wafer 64 so that the next field of semiconductor wafer 64 is brought into position relative to illumination system 42, reticle 68, and projection optical system 46.

It should be understood that the use of photolithography apparatus or exposure apparatus 40, as described above, is not limited to being used in a photolithography system for semiconductor manufacturing. For example, photolithography apparatus 40 may be used as a part of a liquid crystal display (LCD) photolithography system that exposes an LCD device pattern onto a rectangular glass plate or a photolithography system for manufacturing a thin film magnetic head. The illumination source of illumination system 42 may be g-line (436 nanometers (nm)), i-line (365 nm), a KrF excimer laser (248 nm), an ArF excimer laser (193 nm), and an F₂-type laser (157 nm).

With respect to projection optical system 46, when far ultra-violet rays such as an excimer laser is used, glass materials such as quartz and fluorite that transmit far ultra-violet rays is preferably used. When either an F₂-type laser or an x-ray is used, projection optical system 46 may be either catadioptric or refractive (a reticle may be of a corresponding reflective type), and when an electron beam is used, electron optics may comprise electron lenses and deflectors. As will be appreciated by those skilled in the art, the optical path for the electron beams is generally in a vacuum.

In addition, with an exposure device that employs vacuum ultra-violet (VUV) radiation of a wavelength that is approximately 200 nm or lower, use of a catadioptric type optical system may be considered. Examples of a catadioptric type of optical system include, but are not limited to, those described in Japan Patent Application Disclosure No. 8-171054 published in the Official gazette for Laid-Open Patent Applications and its counterpart U.S. Patent No. 5,668,672, as well as in Japan Patent Application Disclosure No. 10-20195 and its counterpart U.S. Patent No. 5,835,275, which are all incorporated herein by reference in their entireties. In these examples, the reflecting optical device may be a catadioptric optical system incorporating a beam splitter and a concave mirror. Japan Patent Application Disclosure (Hei) No. 8-334695 published in the Official gazette for Laid-Open Patent Applications and its counterpart U.S. Patent No. 5,689,377, as well as Japan Patent Application Disclosure No. 10-3039 and its counterpart U.S. Patent No.

5,892,117, which are all incorporated herein by reference in their entireties. These examples describe a reflecting-refracting type of optical system that incorporates a concave mirror, but without a beam splitter, and may also be suitable for use with the present invention.

5 Further, in photolithography systems, when linear motors (see U.S. Patent Nos. 5,623,853 or 5,528,118, which are each incorporated herein by reference in their entireties) are used in a wafer stage or a reticle stage, the linear motors may be either an air levitation type that employs air bearings or a magnetic levitation type that uses Lorentz forces or reactance forces. Additionally, the stage may also move along a guide,
10 or may be a guideless type stage which uses no guide.

Alternatively, a wafer stage or a reticle stage may be driven by a planar motor which drives a stage through the use of electromagnetic forces generated by a magnet unit that has magnets arranged in two dimensions and an armature coil unit that has coil in facing positions in two dimensions. With this type of drive system, one of the magnet
15 unit or the armature coil unit is connected to the stage, while the other is mounted on the moving plane side of the stage.

Movement of the stages as described above generates reaction forces which may affect performance of an overall photolithography system. Reaction forces generated by the wafer (substrate) stage motion may be mechanically released to the floor or ground by
20 use of a frame member as described above, as well as in U.S. Patent No. 5,528,118 and published Japanese Patent Application Disclosure No. 8-166475. Additionally, reaction forces generated by the reticle (mask) stage motion may be mechanically released to the floor (ground) by use of a frame member as described in U.S. Patent No. 5,874,820 and published Japanese Patent Application Disclosure No. 8-330224, which are each
25 incorporated herein by reference in their entireties.

Isolators such as isolators 54 may generally be associated with an active vibration isolation system (AVIS). An AVIS generally controls vibrations associated with forces 112, *i.e.*, vibrational forces, which are experienced by a stage assembly or, more generally, by a photolithography machine such as photolithography apparatus 40 which
30 includes a stage assembly.

A photolithography system according to the above-described embodiments, *e.g.*, a photolithography apparatus which may include one or more dual force actuators, may be built by assembling various subsystems in such a manner that prescribed mechanical accuracy, electrical accuracy, and optical accuracy are maintained. In order to maintain
35 the various accuracies, prior to and following assembly, substantially every optical

system may be adjusted to achieve its optical accuracy. Similarly, substantially every mechanical system and substantially every electrical system may be adjusted to achieve their respective desired mechanical and electrical accuracies. The process of assembling each subsystem into a photolithography system includes, but is not limited to, developing
5 mechanical interfaces, electrical circuit wiring connections, and air pressure plumbing connections between each subsystem. There is also a process where each subsystem is assembled prior to assembling a photolithography system from the various subsystems. Once a photolithography system is assembled using the various subsystems, an overall adjustment is generally performed to ensure that substantially every desired accuracy is
10 maintained within the overall photolithography system. Additionally, it may be desirable to manufacture an exposure system in a clean room where the temperature and humidity are controlled.

Further, semiconductor devices may be fabricated using systems described above, as will be discussed with reference to Fig. 8. The process begins at step 1301 in which
15 the function and performance characteristics of a semiconductor device are designed or otherwise determined. Next, in step 1302, a reticle (mask) in which has a pattern is designed based upon the design of the semiconductor device. It should be appreciated that in a parallel step 1303, a wafer is made from a silicon material. The mask pattern designed in step 1302 is exposed onto the wafer fabricated in step 1303 in step 1304 by a
20 photolithography system. One process of exposing a mask pattern onto a wafer will be described below with respect to Fig. 9. In step 1305, the semiconductor device is assembled. The assembly of the semiconductor device generally includes, but is not limited to, wafer dicing processes, bonding processes, and packaging processes. Finally, the completed device is inspected in step 1306.

Fig. 9 is a process flow diagram which illustrates the steps associated with wafer processing in the case of fabricating semiconductor devices in accordance with an embodiment of the present invention. In step 1311, the surface of a wafer is oxidized. Then, in step 1312 which is a chemical vapor deposition (CVD) step, an insulation film may be formed on the wafer surface. Once the insulation film is formed, in step 1313,
30 electrodes are formed on the wafer by vapor deposition. Then, ions may be implanted in the wafer using substantially any suitable method in step 1314. As will be appreciated by those skilled in the art, steps 1311-1314 are generally considered to be preprocessing steps for wafers during wafer processing. Further, it should be understood that selections made in each step, *e.g.*, the concentration of various chemicals to use in forming an
35 insulation film in step 1312, may be made based upon processing requirements.

At each stage of wafer processing, when preprocessing steps have been completed, post-processing steps may be implemented. During post-processing, initially, in step 1315, photoresist is applied to a wafer. Then, in step 1316, an exposure device may be used to transfer the circuit pattern of a reticle to a wafer. Transferring the circuit
5 pattern of the reticle of the wafer generally includes scanning a reticle scanning stage which may, in one embodiment, include a force damper to dampen vibrations.

After the circuit pattern on a reticle is transferred to a wafer, the exposed wafer is developed in step 1317. Once the exposed wafer is developed, parts other than residual photoresist, *e.g.*, the exposed material surface, may be removed by etching. Finally, in
10 step 1319, any unnecessary photoresist that remains after etching may be removed. As will be appreciated by those skilled in the art, multiple circuit patterns may be formed through the repetition of the preprocessing and post-processing steps.

Although only a few embodiments of the present invention have been described, it should be understood that the present invention may be embodied in many other specific
15 forms without departing from the spirit or the scope of the present invention. By way of example, although the use wafer table arrangement with a substantially flat overall planar surface of a uniform height has been described as being suitable for use in an immersion lithography system to enable a small liquid-filled or fluid-filled gap to be maintained between a projection lens and the surface of the wafer table, such a wafer table is not
20 limited to use as a part of an immersion lithography system.

A table which supports an object to be scanned and has a relatively planar, substantially uniform top surface has generally been described as being a wafer table. Such a table is not limited to being a wafer table. For instance, a reticle table may also have a relatively planar, substantially uniform top surface. Alternatively, a substrate table
25 supporting, for example, a glass plate for LCD manufacturing, a microscope specimen, or the like may also have a substantially flat planar surface.

Components which are supported within a wafer table arrangement such that top surfaces of the components are at substantially the same height as a top surface of a wafer table have been described as including dose sensors, dose uniformity sensors, aerial
30 image sensors, reference flats, and fiducial marks. It should be appreciated, however, that any suitable additional components may be supported within the wafer table arrangement such that the top surfaces of the additional components are substantially flush with the top surface of the wafer table. Further, while a wafer table arrangement may include a dose sensor or a dose uniformity sensor, an aerial image sensor, a reference flat, and a fiducial
35 mark, a wafer table arrangement may not necessarily include a dose sensor or a dose

uniformity sensor, an aerial image sensor, a reference flat, and a fiducial mark. That is, a wafer table arrangement may include as little as one component that has a top surface which is substantially flush with the overall top surface of the wafer table arrangement.

5 It should be appreciated that for substantially all components supported on a wafer table to have top surfaces that are substantially flush with the overall top surface of the a wafer table arrangement, the wafer table arrangement may need to support a bottom surface of each component at different heights. That is, the bottom surfaces of the components may need to be supported at different heights in order to enable the top surfaces of the components to be oriented such that the top surfaces are all substantially
10 flush with the overall top surface of the wafer table arrangement.

The materials used to form a wafer table arrangement, *e.g.*, a wafer table and a plate which is positioned atop the wafer table, may be widely varied. Although a plate has been described as being formed from Teflon, it should be appreciated that the plate may be formed from substantially any suitable material. Therefore, the present examples
15 are to be considered as illustrative and not restrictive, and the invention is not to be limited to the details given herein, but may be modified within the scope of the appended claims.

WHAT IS CLAIMED IS:

1. An apparatus comprising:
a lens; and
a wafer table, the wafer table assembly having a surface, the wafer table
5 assembly being arranged to support a wafer to be moved with respect to the lens and at
least one component, a surface of the wafer and a surface of the at least one component
each being at substantially a same height as the surface of the wafer table assembly,
wherein the wafer table is suitable for an immersion application.
2. The apparatus of claim 1 wherein an overall top surface of the
10 wafer table assembly which includes the surface of the wafer, the surface of the wafer
table assembly, and the surface of the at least one component is substantially planar.
3. The apparatus of claim 1 wherein the at least one component is a
sensor.
4. The apparatus of claim 1 wherein the at least one component is at
15 least one of a reference flat, an aerial image sensor, a dose sensor, and a dose uniformity
sensor.
5. The apparatus of claim 4 wherein the wafer table assembly is
further arranged to support a fiducial mark, the fiducial mark having a top surface, the top
surface of the fiducial mark being at substantially the same height as the surface of the
20 wafer table assembly.
6. The apparatus of claim 1 wherein the wafer table assembly defines
a plurality of openings, and a first opening of the plurality of openings is arranged to
substantially support the wafer and a second opening of the plurality of openings is
arrange to substantially support the at least one component.
7. The exposure apparatus of claim 1 wherein the wafer table
25 assembly is arranged to support a wafer holder, the wafer holder being arranged to hold
the wafer such that the surface of the wafer is at substantially the same height as the
surface of the wafer table assembly.
8. The apparatus of claim 1 further including:
30 a retaining ring, the retaining ring being arranged enable a liquid to be
substantially held between a first surface of the lens and an overall top surface of the

wafer table assembly, wherein the overall top surface of the wafer table assembly includes the surface of the wafer, the surface of the wafer table assembly, and the surface of the at least one component.

9. A device manufactured with the apparatus of claim 1.
- 5 10. A object on which an image has been formed by the apparatus of claim 1.
11. An apparatus comprising:
a lens; and
a table assembly, the table assembly having a surface, the table assembly
10 being arranged to support an object to be scanned with respect to the lens and at least one component, a surface of the object and a surface of the at least one component each being at substantially a same height as the surface of the table assembly, wherein the table assembly is suitable for use in an immersion application.
12. The apparatus of claim 11 wherein the at least one component is a
15 sensor.
13. The apparatus of claim 11 wherein the at least one component is at least one of a reference flat, an aerial image sensor, a dose sensor, and a dose uniformity sensor.
14. The apparatus of claim 13 wherein the table assembly is further
20 arranged to support a fiducial mark, the fiducial mark having a surface, the surface of the fiducial mark being at substantially the same height as the surface of the table assembly.
15. The apparatus of claim 14 wherein an overall top surface of the table assembly includes the top surface of the fiducial mark, the surface of the object, the surface of the table assembly, and the surface of the at least one component.
- 25 16. The apparatus of claim 11 further including:
a retaining ring, the retaining ring being arranged enable a liquid to be substantially held between a first surface of the lens and an overall top surface of the table assembly wherein the overall top surface of the table assembly includes the top surface of the object, the top surface of the table assembly, and the top surface of the at least one
30 component.

17. A device manufactured with the apparatus of claim 11.
18. A wafer on which an image has been formed by the apparatus of claim 11
19. An immersion lithography apparatus comprising:
5 a lens, the lens having a first surface and an associated effective numerical aperture;
a liquid, the liquid being suitable for enhancing the effective numerical aperture of the lens; and
a table arrangement, the table arrangement having a substantially flat top
10 surface, the substantially flat top surface being arranged to substantially oppose the first surface, the liquid being arranged substantially between the substantially flat top surface and the first surface, wherein the substantially flat top surface includes a top surface of an object to be scanned and a top surface of at least one sensor.
20. The immersion lithography apparatus of claim 19 wherein the at
15 least one sensor is at least one of a reference flat, an aerial image sensor, a dose sensor, and a dose uniformity sensor.
21. The immersion lithography apparatus of claim 20 wherein the table
arrangement is further arranged to support a fiducial mark having a top surface, and
wherein the substantially flat top surface further includes the top surface of the fiducial
20 mark.
22. The immersion lithography apparatus of claim 19 wherein the
object is a wafer.
23. A device manufactured with the immersion lithography apparatus
of claim 19.
24. A wafer on which an image has been formed using the immersion
25 lithography apparatus of claim 19.
25. An apparatus comprising:
a lens; and
a wafer table assembly, the wafer table assembly including a base and a
30 plate having a plate surface, the base being arranged to support the plate, at least one

component, and a wafer to be moved with respect to the lens, a wafer surface of the wafer and a component surface of the at least one component each being at substantially a same height as the plate surface, wherein the lens and the wafer table assembly are arranged to be used in an immersion application.

5 26. The apparatus of claim 25 wherein an overall top surface of the wafer table assembly which includes the wafer surface, the plate surface, and the component is substantially planar.

 27. The apparatus of claim 25 wherein the at least one component is a sensor.

10 28. The apparatus of claim 25 wherein the at least one component is at least one of a reference flat, an aerial image sensor, a dose sensor, and a dose uniformity sensor.

 29. The apparatus of claim 28 wherein the wafer table assembly is further arranged to support a fiducial mark, the fiducial mark having a fiducial mark surface, the fiducial mark surface being at substantially the same height as the plate surface.

15 30. The apparatus of claim 25 wherein the plate defines a plurality of openings, and the wafer is arranged within a first opening of the plurality of openings and the at least one component is arranged within a second opening of the plurality of openings.

 31. The apparatus of claim 25 wherein the wafer table assembly is arranged to support a wafer holder, the wafer holder being arranged to hold the wafer such that the top wafer surface is at substantially the same height as the surface of the wafer table assembly.

25 32. The apparatus of claim 25 further including:
 a retaining ring, the retaining ring being arranged enable a liquid to be substantially held between a first surface of the lens and an overall top surface of the wafer table assembly, wherein the overall top surface of the table assembly includes the surface of the object, the surface of the table assembly, and the surface of the at least one component.

30

 33. A device manufactured with the apparatus of claim 25.

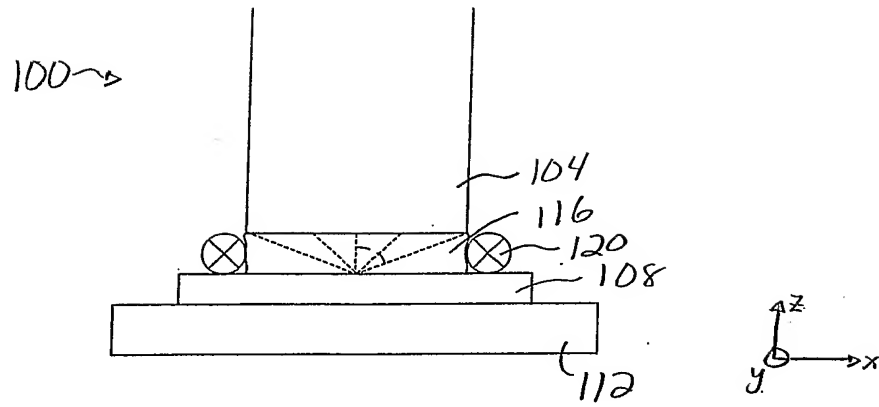


Fig. 1

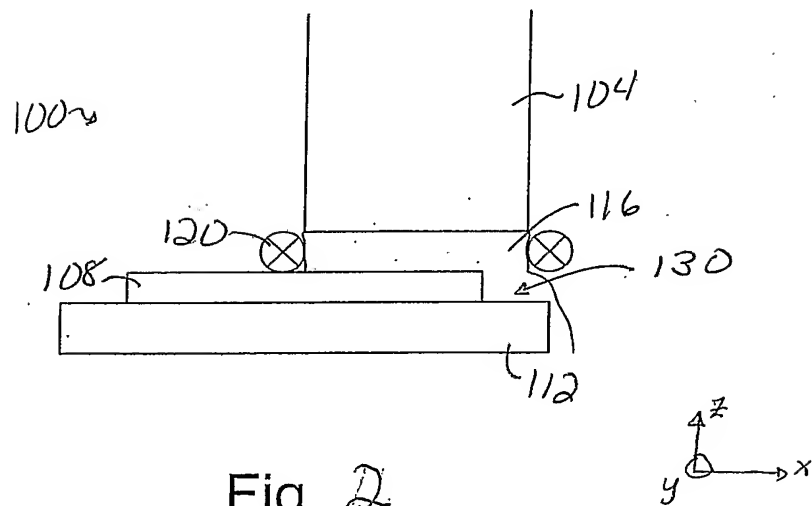
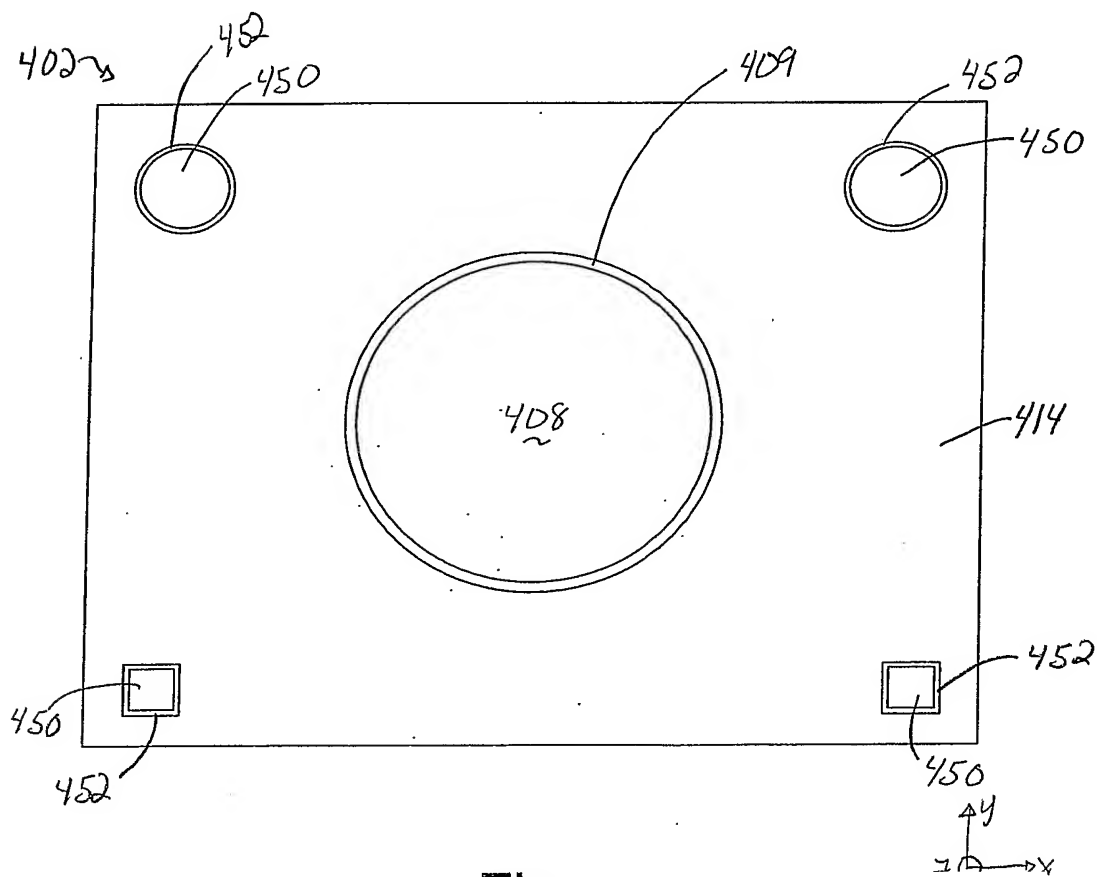
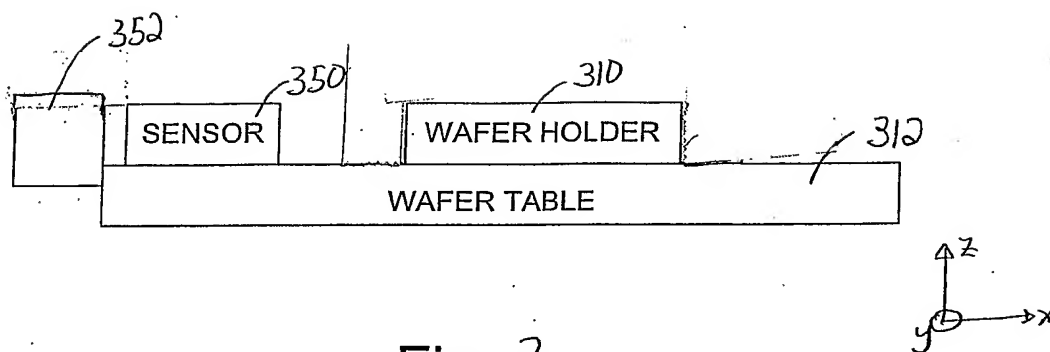


Fig. 2



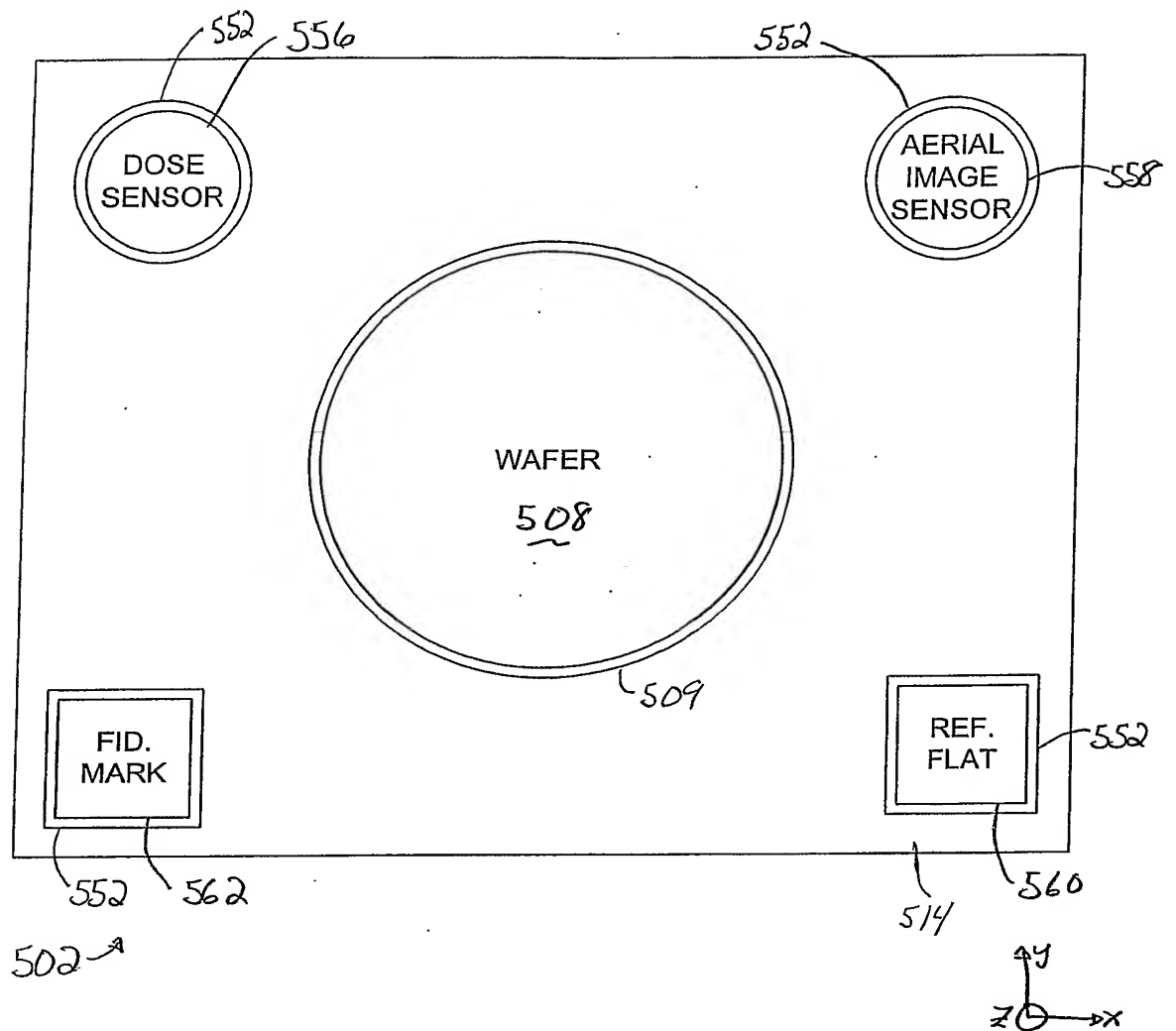


Fig. 5

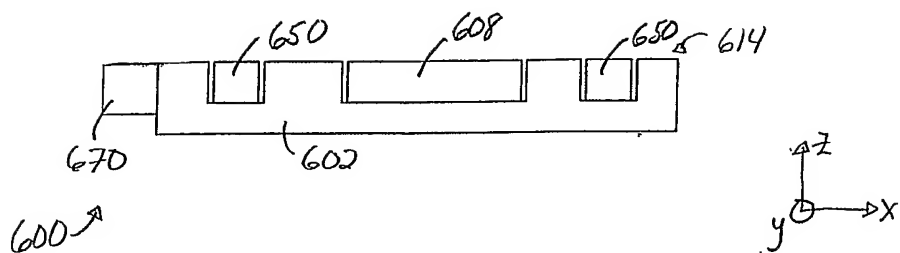


Fig. 6a

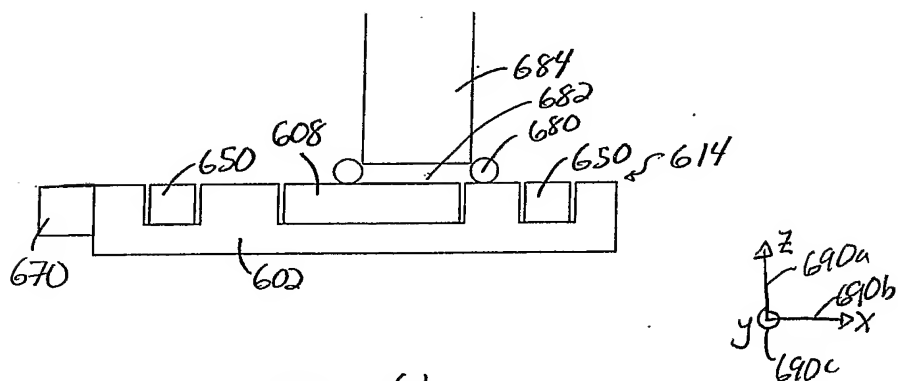


Fig. 6b

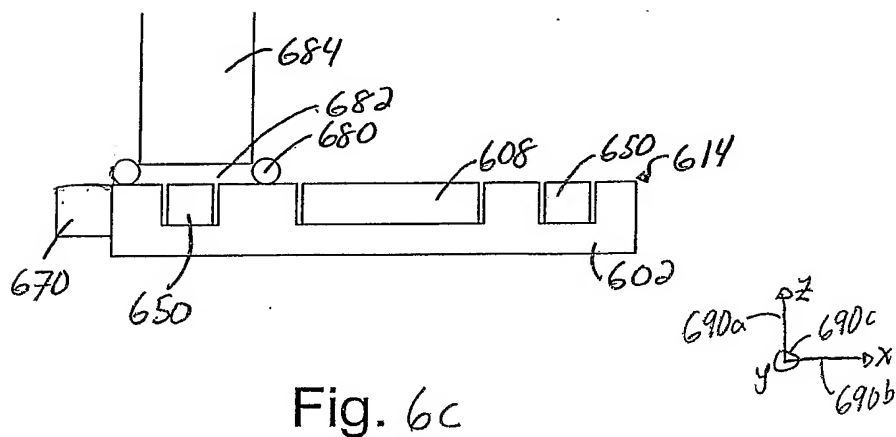


Fig. 6c

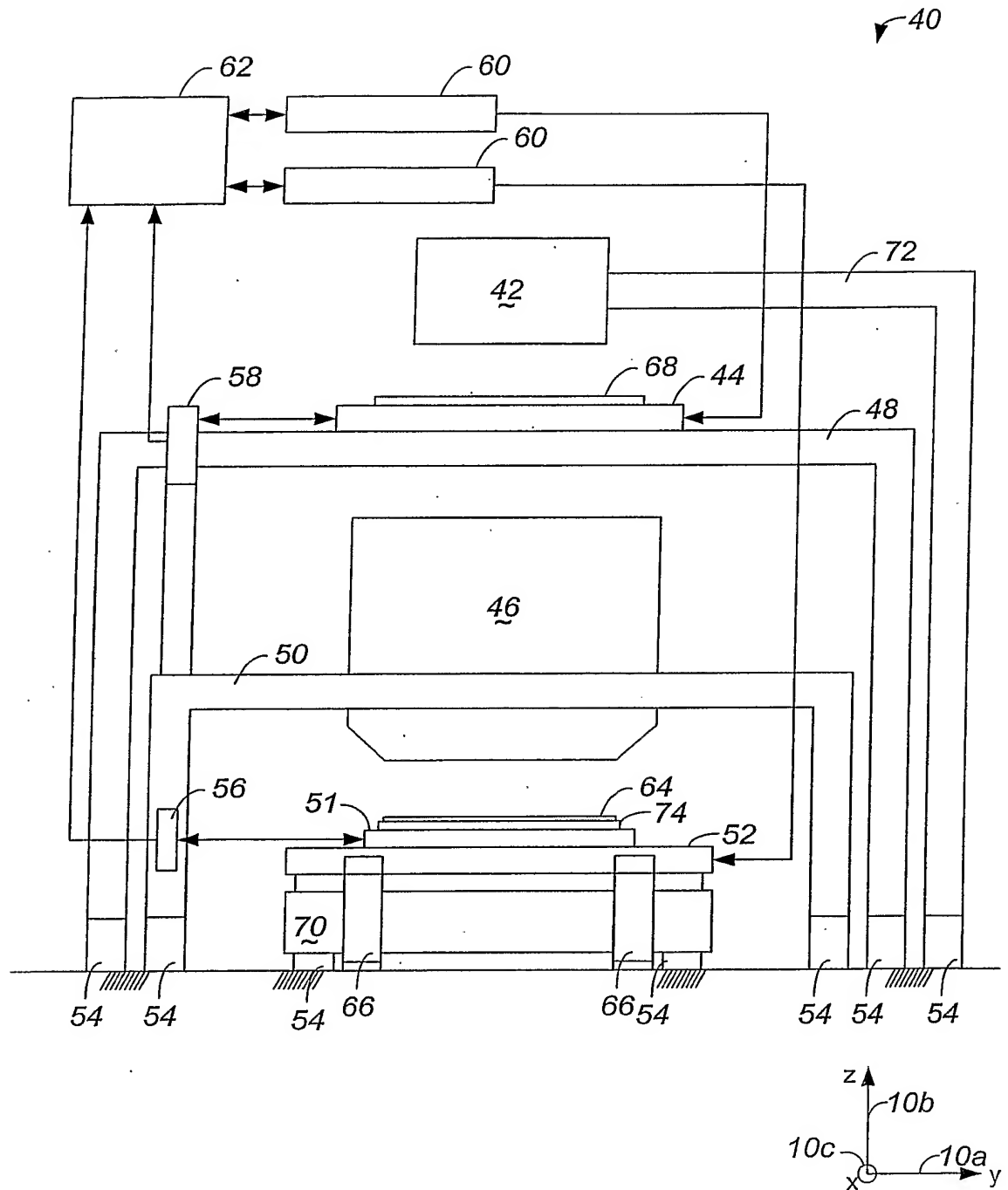


Fig.

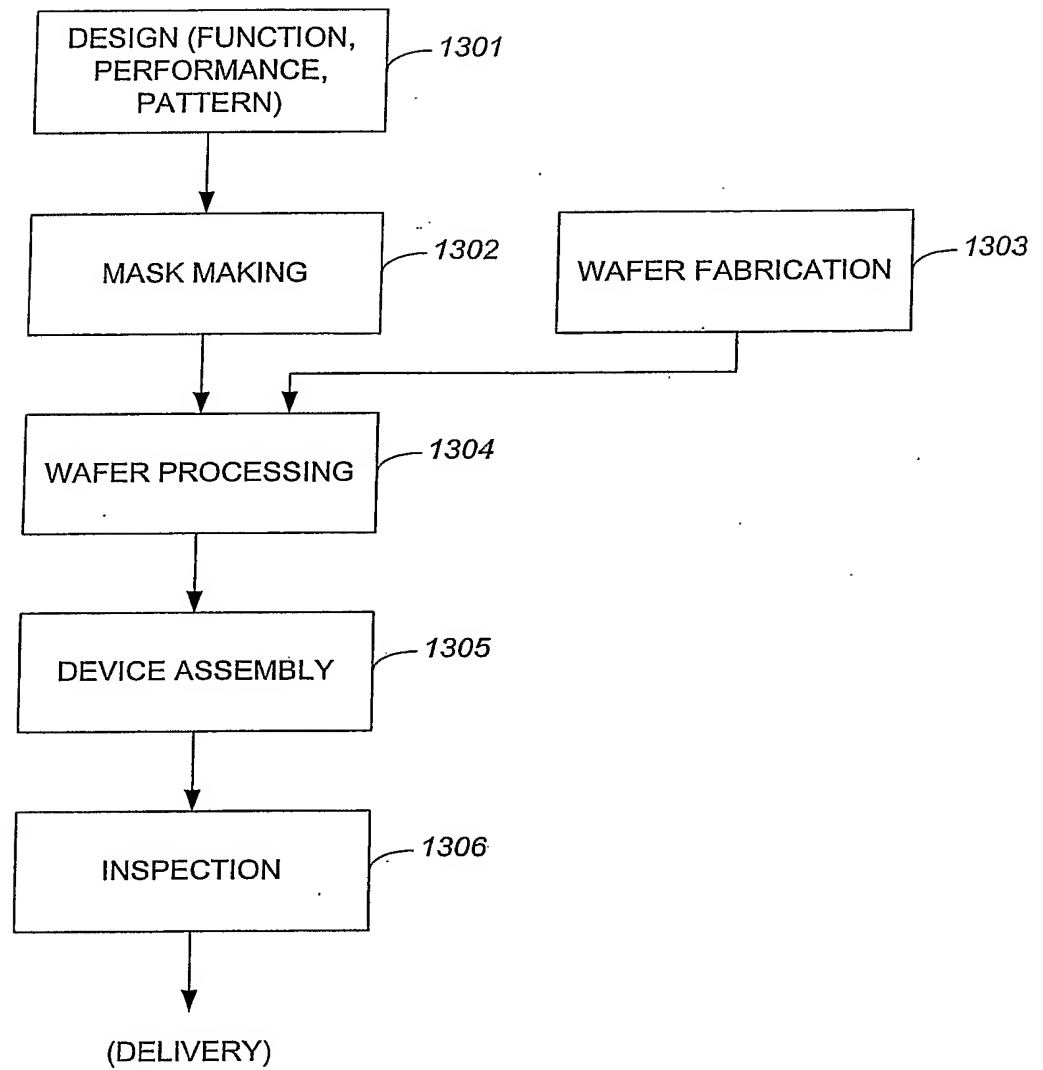


Fig 8.

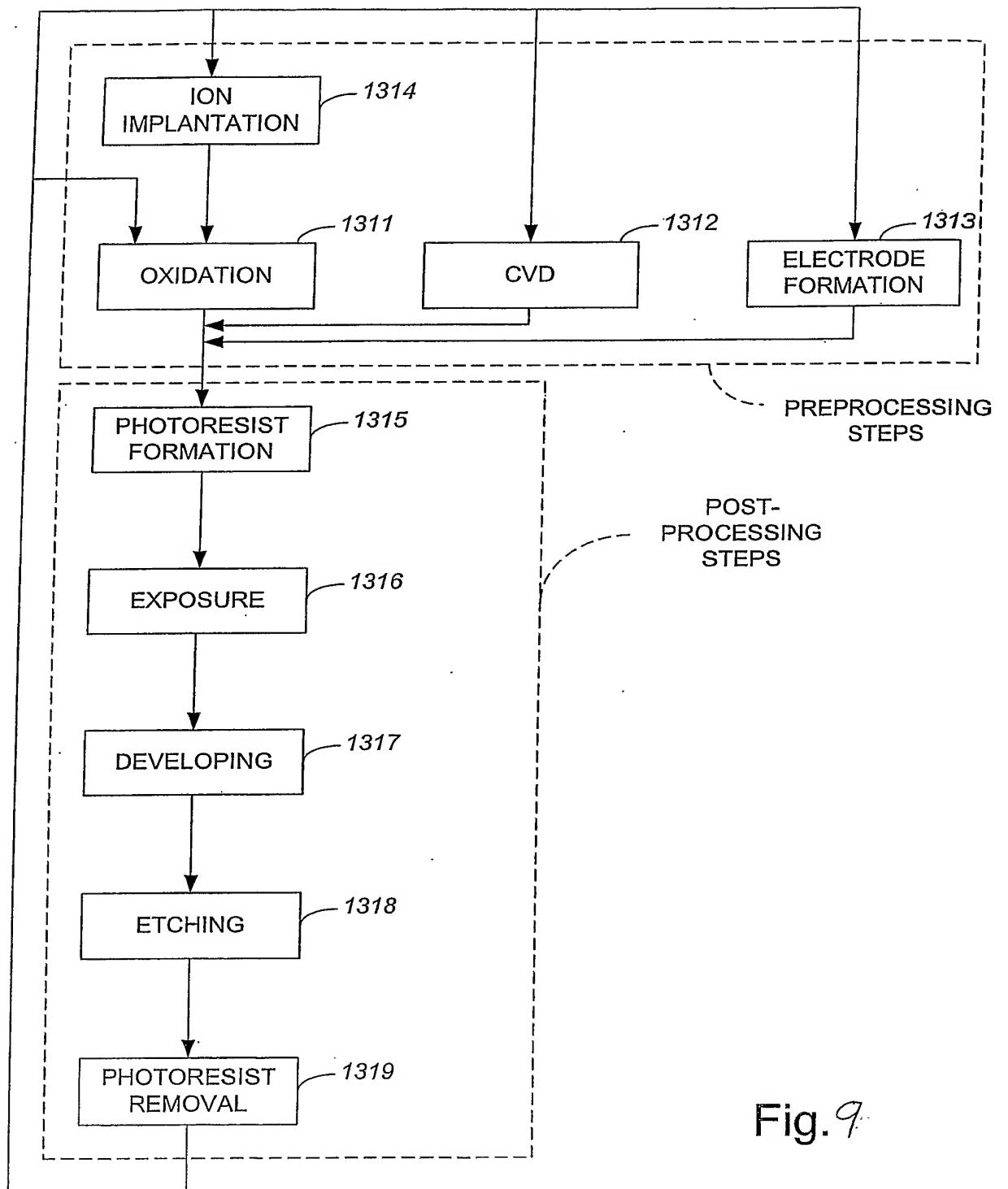


Fig. 9

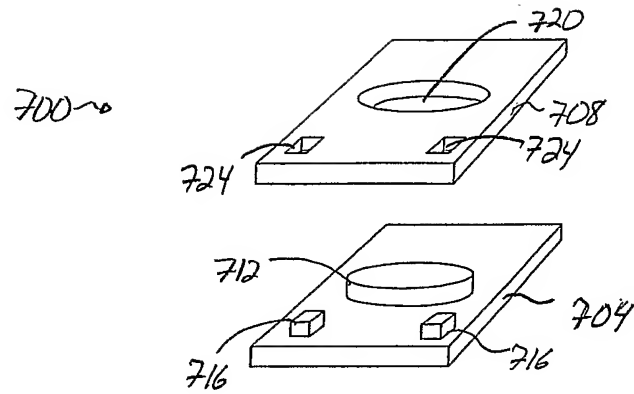


Fig. 10a

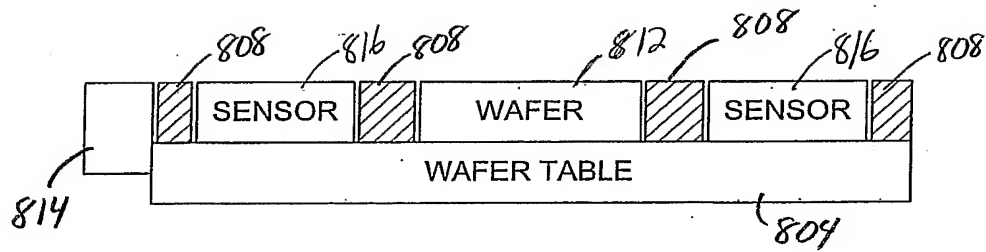


Fig. 10b

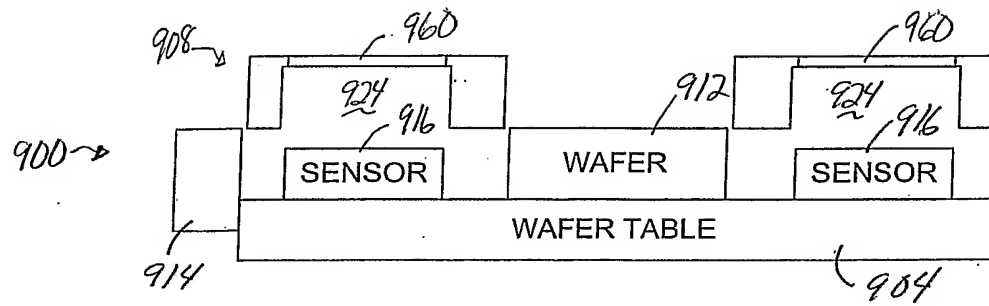


Fig. 11a

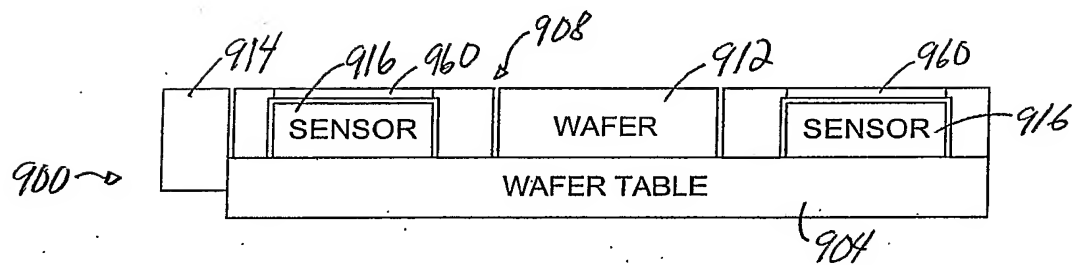


Fig. 11b